

A17B-8102-0602 Fanuc Beta i SVSP-02i Step2 Mainboard PCB Datasheet



Brand: Fanuc

SKU: [1037917910416](#)

Category: CNC, Robotics & PLC Boards

Price: **\$171.99**

E-mail: sales@equipspares.com

Web: <https://www.equipspares.com>

Product Page:

<https://www.equipspares.com/product/a17b-8102-0602-fanuc-beta-i-svsp-02i-step2-mainboard-pcb>

Product Description

Fanuc A17B-8102-0602 features Beta i SVSP-02i compatibility, Step2 architecture, and a 24 VDC nominal input. The printed circuit board is constructed from high-density FR4 fiberglass, utilizing surface mount technology for optimized thermal dissipation and signal integrity. It incorporates multi-pin header interfaces for direct integration with servo amplifier modules, alongside solid-state logic components and an integrated digital signal processor to facilitate high-speed multi-axis synchronization. The hardware operates within a standard 0 to 55 °C thermal envelope, ensuring stable electrical performance under continuous load conditions.

A17B-8102-0602 Specifications

Model Number: A17B-8102-0602

Brand: Fanuc

Product Category: Mainboard / Printed Circuit Board (PCB)

Series Compatibility: Beta i Series

Amplifier Compatibility: SVSP-02i (Combined Servo/Spindle Amplifier)

Architecture: PLUS STEP2 Series

Nominal Input Voltage: 24 VDC

Board Material: FR4 Fiberglass

Mounting Technology: Surface Mount Technology (SMT)

Operating Temperature: 0 to 55 °C

Storage Temperature: -20 to 60 °C

Relative Humidity: 10 % to 75 % (Non-condensing)

Interface Type: Multi-pin Headers / Edge Connectors

Control Type: Multi-Axis Synchronization

Component Type: Solid-State Logic / DSP

Weight: 0.45 kg

Country of Origin: Japan

A17B-8102-0602 Applications

Primary applications include integration into Beta i SVSP-02i combined servo and spindle amplifier modules for multi-axis CNC machining centers. Deployed within automated milling machines, robotic articulation controllers, and high-precision turning lathes, this mainboard facilitates direct logic processing and motor synchronization.

Supplemental Images

